

ABSTRACT

An apparatus comprises a memory cell and ground control circuitry coupled to the memory cell to programmably control a voltage at a first ground. The apparatus further comprises first circuitry coupled to the memory cell to provide a first voltage to the memory cell during a first period. The first voltage referenced to the first ground. The apparatus additionally comprises second circuitry coupled to the memory cell to provide a second voltage to memory cell during a second period. The second voltage referenced to a second ground different from the first ground.